Concertal Systems, Inc.

System Design Automation

Innovating!
Together.

IP Reuse for the Masses

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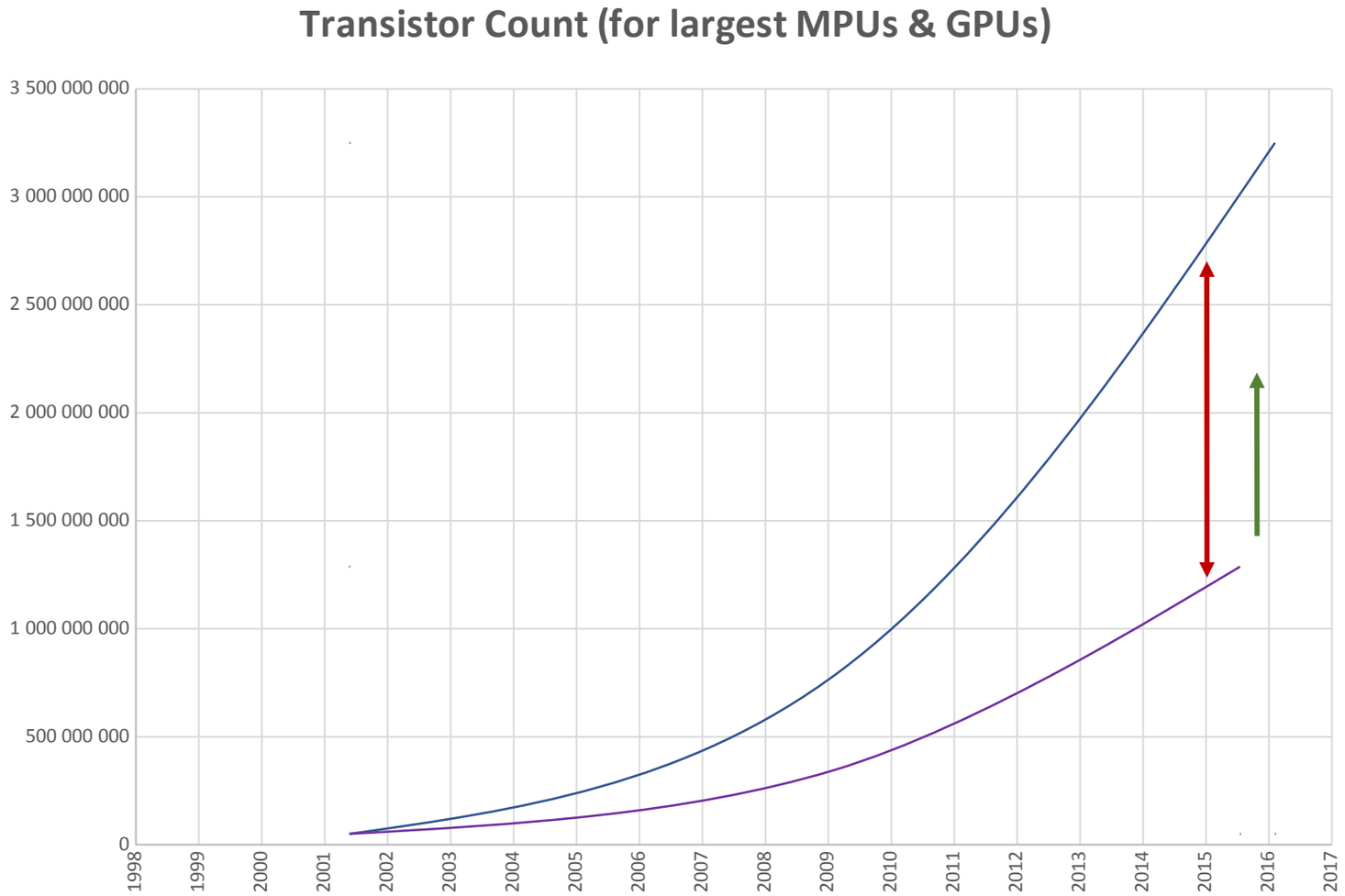
Elements for Mass Acceptance

SIMPLE

SCALABLE

ACCESSIBLE

More Complex Than Ever!



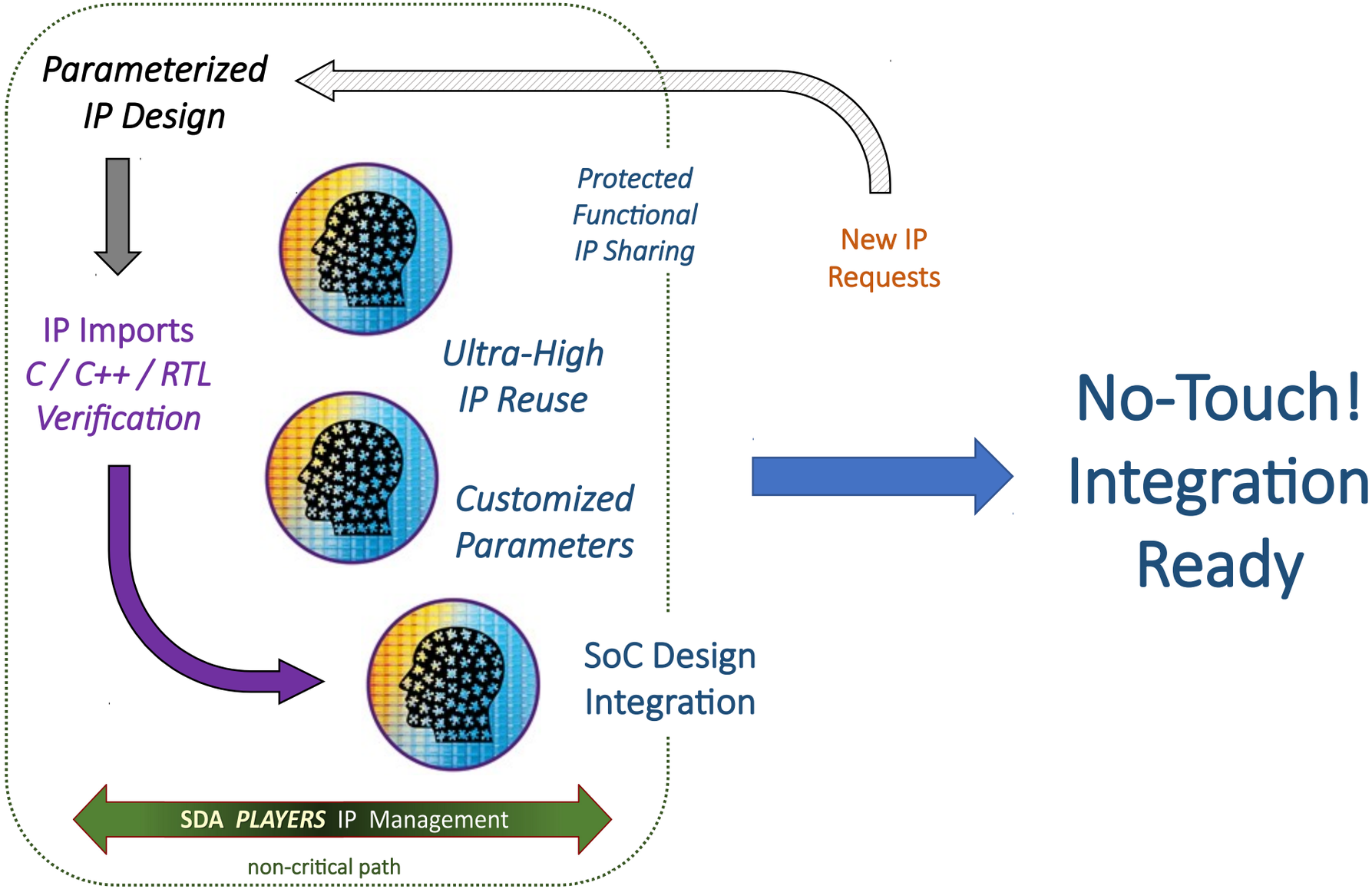
Continued Complexity

Productivity Gap
Affects all designs!

System Design Automation

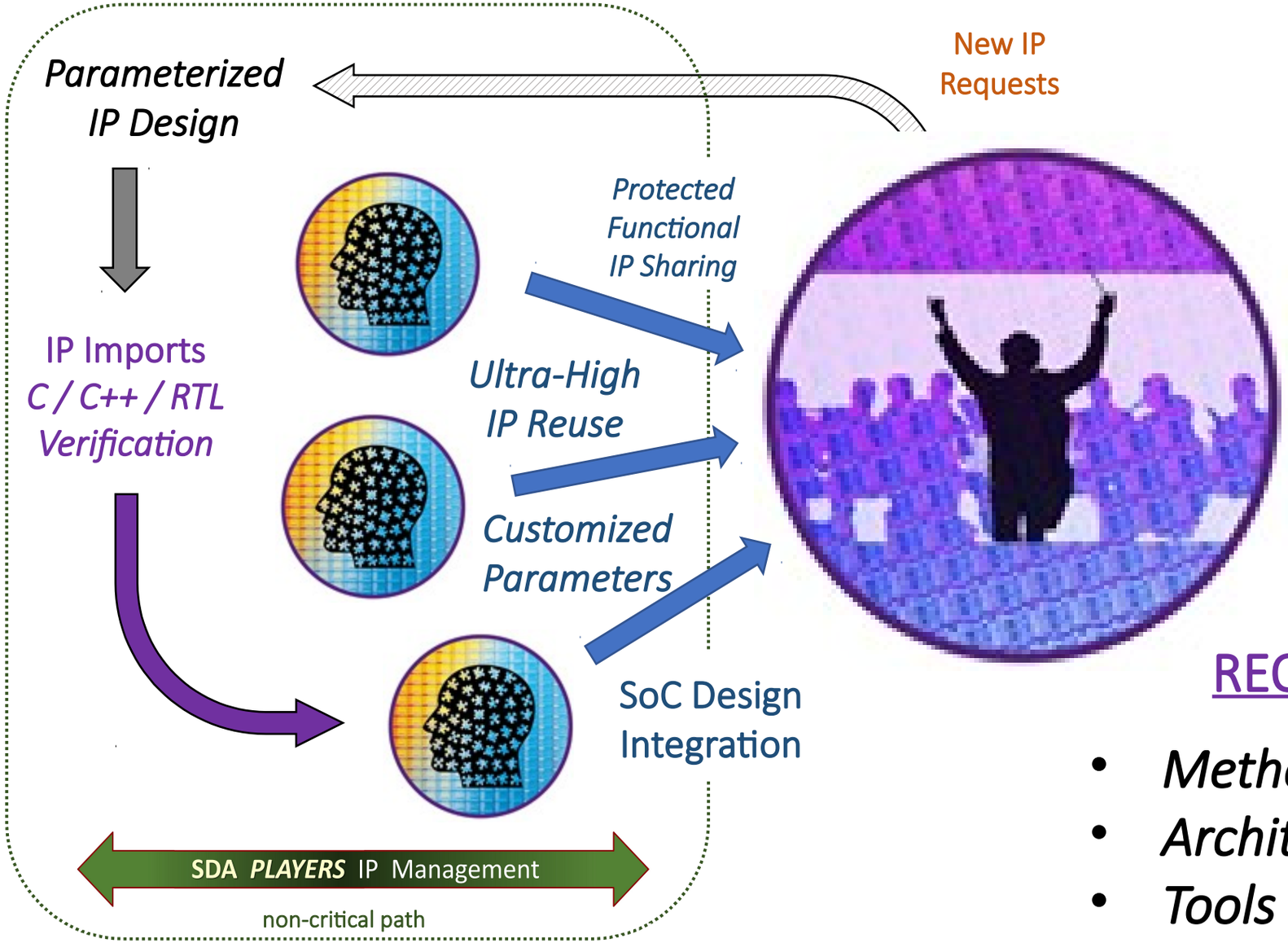
Design Productivity
IP reuse required!

Non-Critical Path IP Development



No-Touch! Integration Ready

Automated Critical Path Design



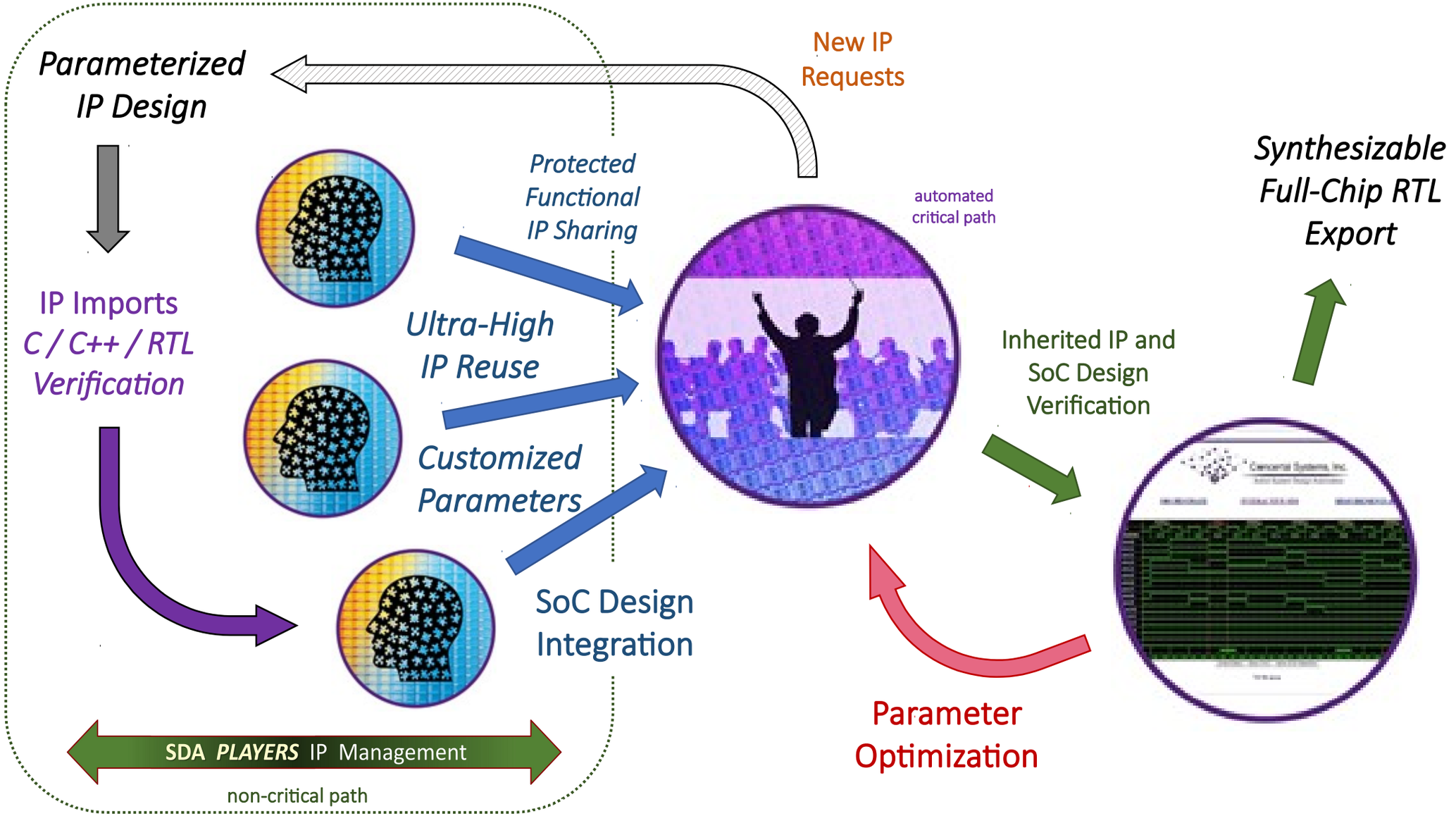
ZERO EFFORT
Automation
On-Demand!

Design
&
Test Bench
Generation

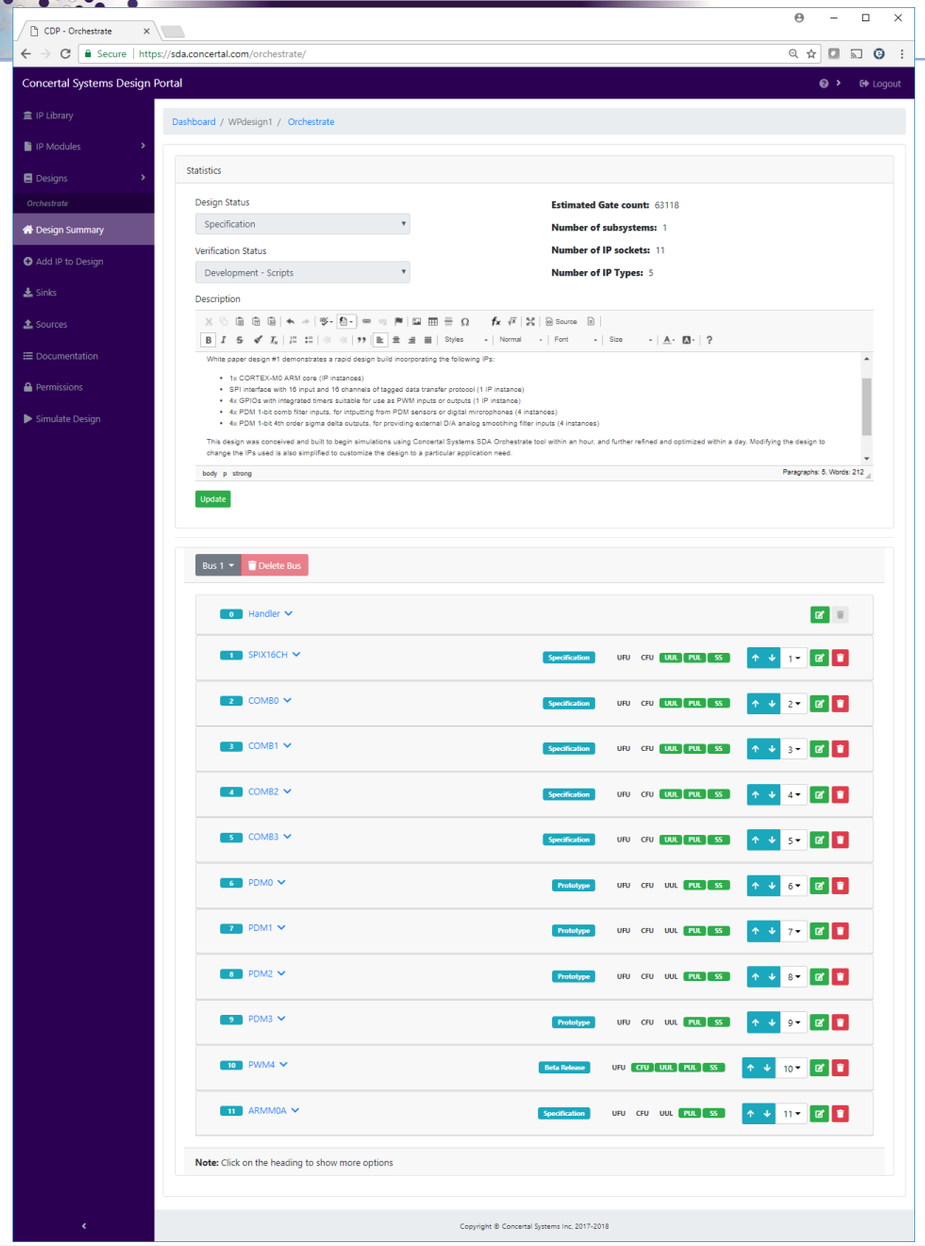
REQUIRES:

- *Methods of Operation*
- *Architecture Topology*
- *Tools*

Integrated Simulation Environment

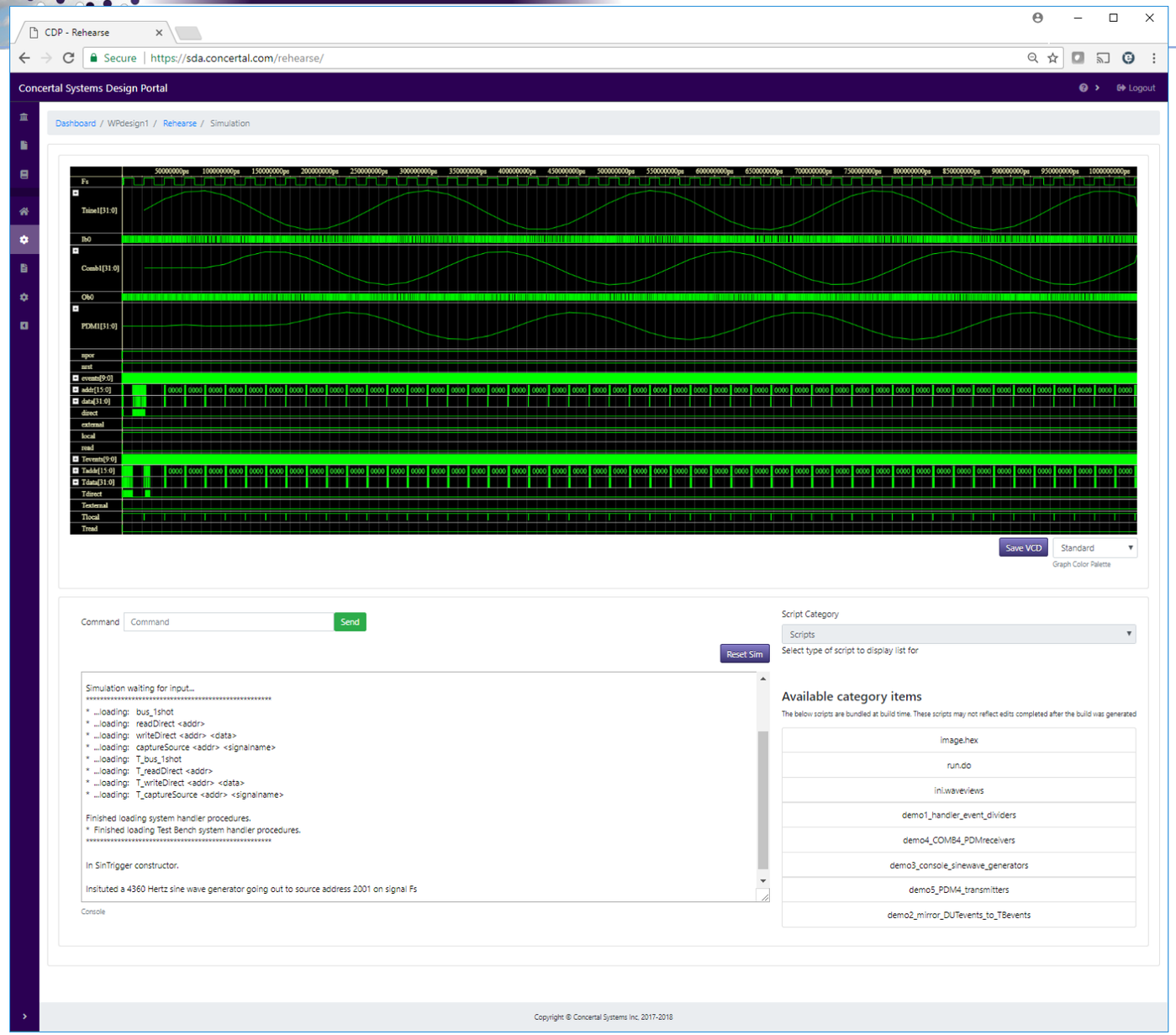


How Simple?



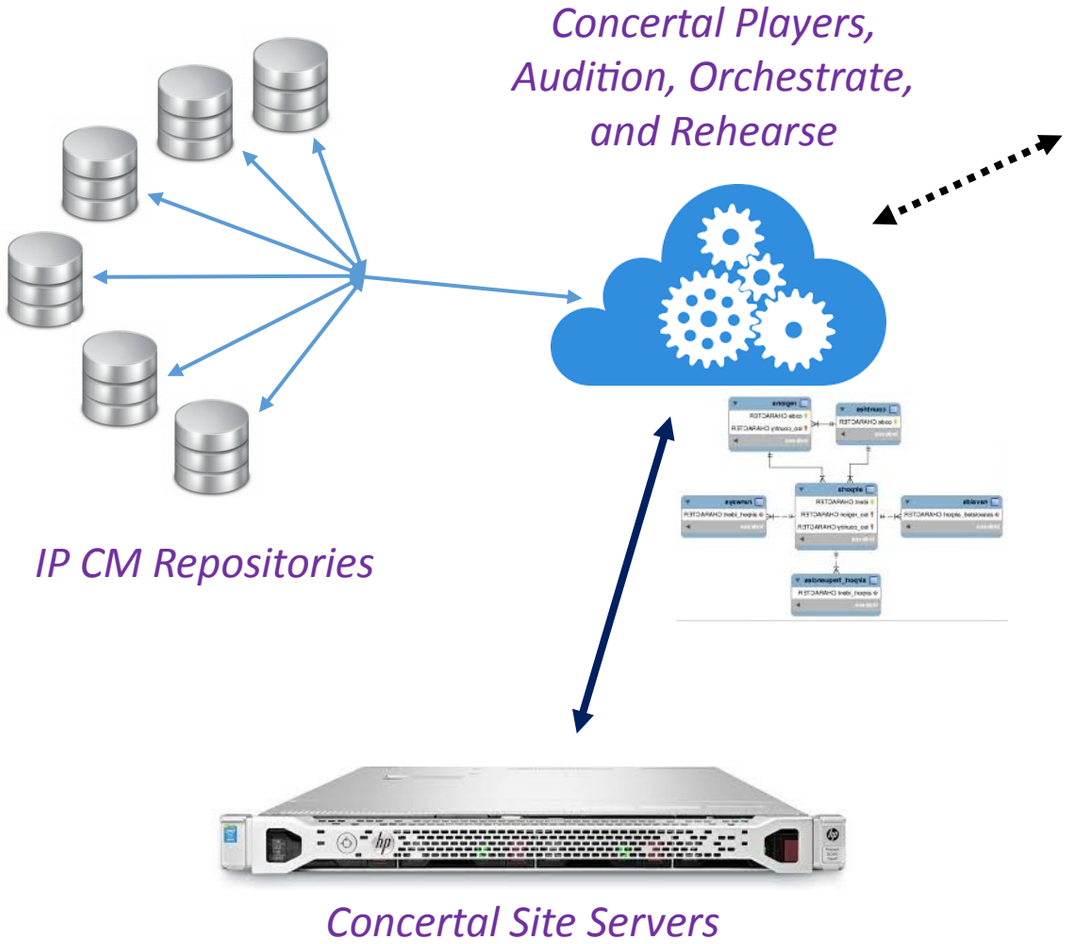
- Orchestrate designs by just adding IP and auto building simulation ready RTL design with test bench.
- Not simply IP stitching. Designs built upon a highly flexible and configurable method of operation and efficient architecture.
- Designs may be simply tuned by defining configuration settings and limiting flexibility as desired.
- IP wrappers translate IP to SDA method of operation.
- Integrated compiled simulation engine allows for inherited IP scripts for verifying IP in it's integrated form.
- Automatic user guide documentation.

How Simple?



- Integrated compiled simulation engine allows for inherited IP scripts for verifying IP in it's integrated form.
- Inherited foundation and IP specific tasks for aiding system simulation and UVM build environments
- Interactive TCL based scripting environment.
- C/C++ model capable

How Accessible?



- Web secure accessible – no software installs!
  - ORCHESTRATE SoC Developer – full hands-off SoC/FPGA design from PLAYERS IP library elements.
  - AUDITION IP Developer – IP specification and import of IP and IP test benches.
  - REHEARSE Simulation – interactive and TCL script based simulation environment
- All SDA accomplished server side
  - IP source code protected until export.
  - Controlled IP and SoC design sharing.
- SDA ecosystem available to all project stakeholders.

SDA Value Proposition

- SIMPLIFY AND ACCELERATE
- UNIQUELY COMPLIMENTARY
- STRONGER TOGETHER
- CONSISTENT QUALITY
- EXPERIENCED EXPERTS

IP Reuse for the Masses!

Automated front-end SoC/FPGA design through IP reuse

Scalable infrastructure and community

Secure web-based accessibility for IP providers and SoC/FPGA developers

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THANK YOU!

Q & A