



**Pls vote for CryptOne for
”Best Innovative IP
prize”**

100% safe IP Core? – CryptOne as an example of new generation of secured IP Cores

Jacek Hanke, CEO Digital Core Design

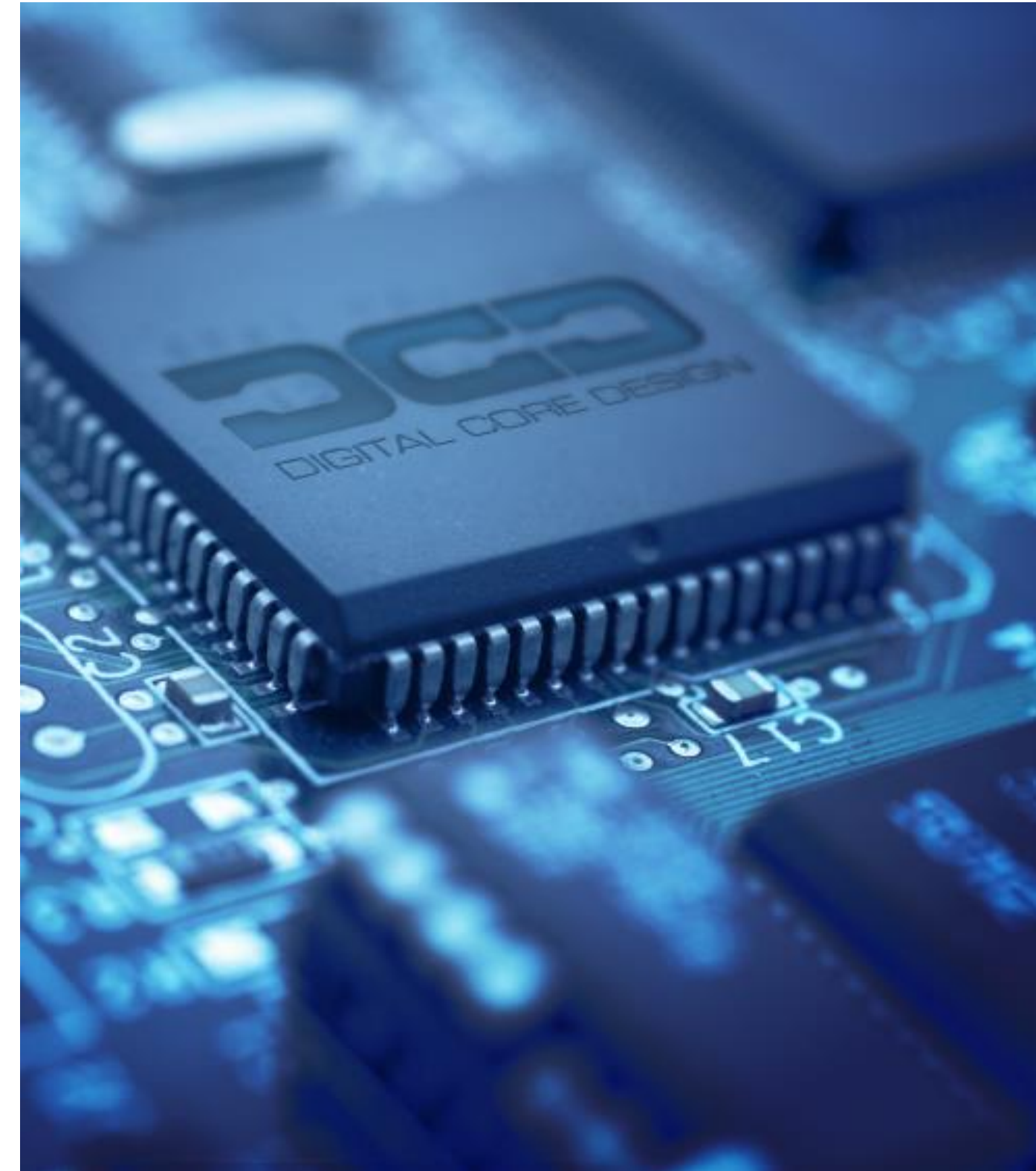


Shanghai, China, September 12th, 2019



Agenda

- 1.About Digital Core Design
- 2.Milestones
- 3.Security vs hardware
- 4.CryptOne
- 5.Summary



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Digital Core Design

- ✓ Digital Core Design has been founded in 1999 and since the early beginnings is focused in IP Cores improvement and System-on-Chip designs,
- ✓ During these two decades, DCD's launched more than 70 different architectures, among them e.g. World's Fastest 8051 – the DQ80251 and royalty-free 32-bit CPU - the D32PRO,
- ✓ DCD has sold more than 1000 license to various customers like corporations start-ups, R&D offices, universities and so on.



Milestones

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CryptOne, 100% safe crypto CPU



D32PRO awarded with the „Teraz Polska” Prize



D32PRO, royalty-free 32-bit CPU



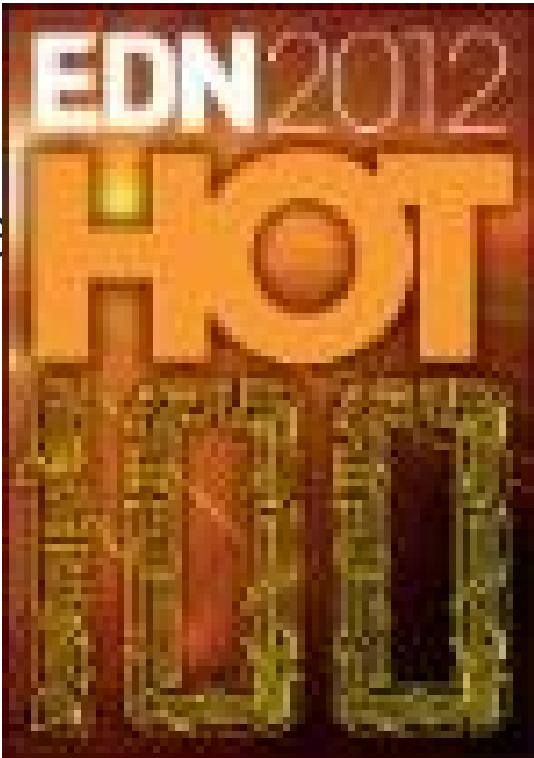
D32PRO named Polish Product of the Future

DCD among 4 most innovative companies in Poland



DQ80251 presented at CeBIT 2013 opening ceremony

DRPIC 166X IP Core EDN Hot 100 Products



Product of the Year Award of 2012 for DQ80251 & DoCD



DoCD™ Hardware Debugger



DP8051XP IP Core

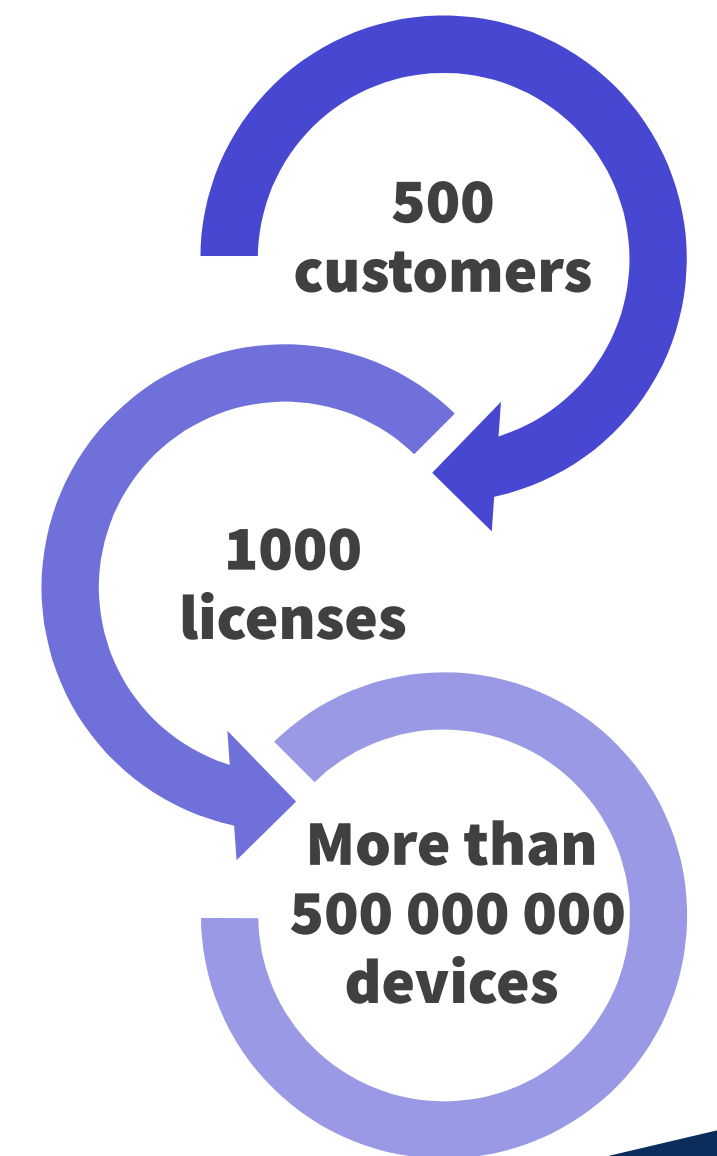


1999 DCD established



DCD's IP Cores

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DCD's IP Cores

- ✓ One of the most experienced companies in IP Core market
 - ✓ More than 70 architectures in portfolio including 8-bit, 16-bit, 32-bit MCU, UART, I2C, SPI, I3C,
 - ✓ USB, CAN, CAN-FD LIN, floating points ...
 - ✓ Among them [World's Fastest & World's Smallest 8051 & 80251](#)
 - ✓ Deeply embedded, [royalty-free, fully scalable 32-bit CPU](#)
 - ✓ [Technology independent](#) (ASIC & FPGA)
 - ✓ IP Cores tailored to the project needs
 - ✓ Royalty-free solutions



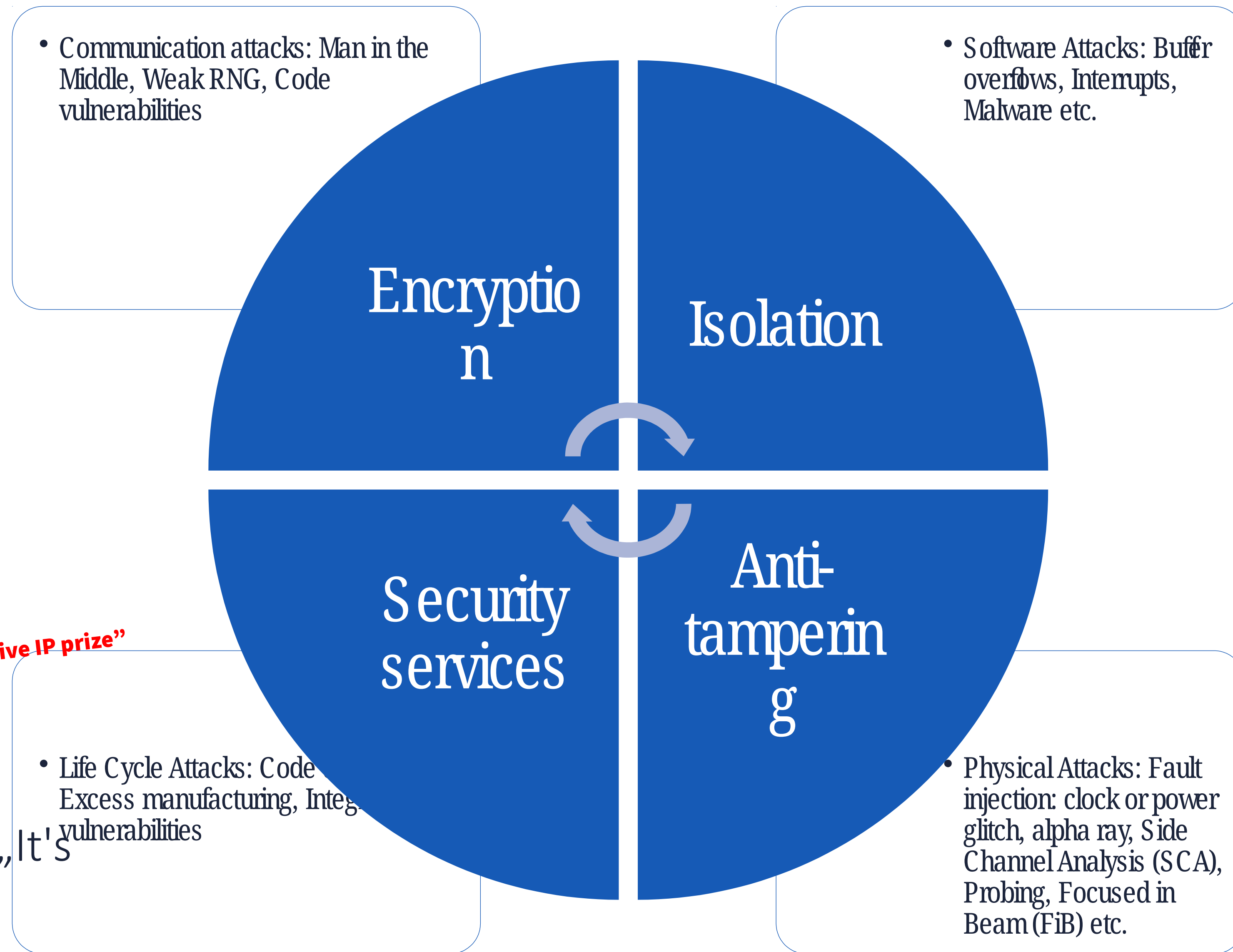
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Security, stupid*

- ✓ There are a range of attack types...

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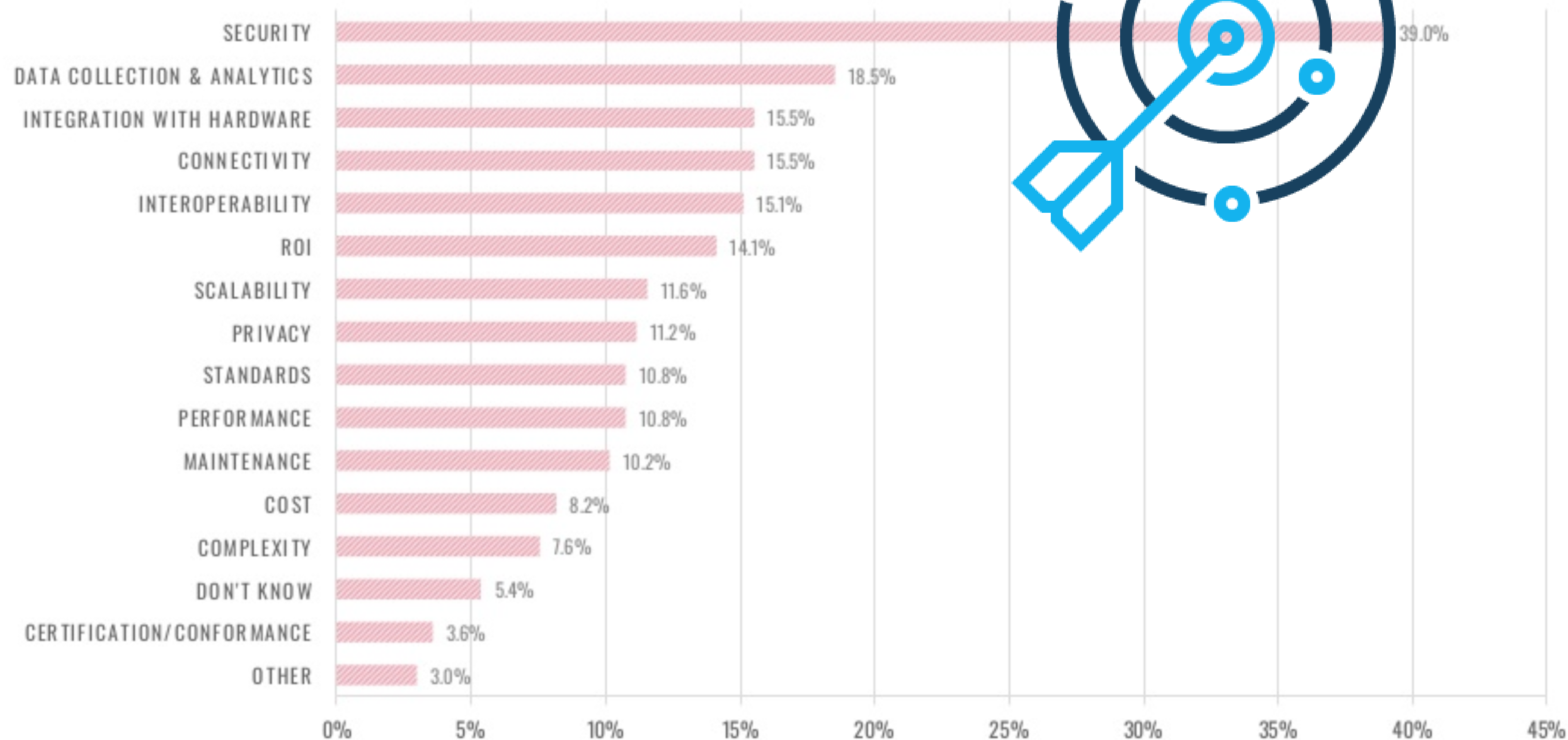
* Paraphrased: Bill Clinton 1993 „It's the economy, stupid”



Security
is
important?

TOP IoT CONCERNS

What are your top 2 concerns for developing IoT solutions?

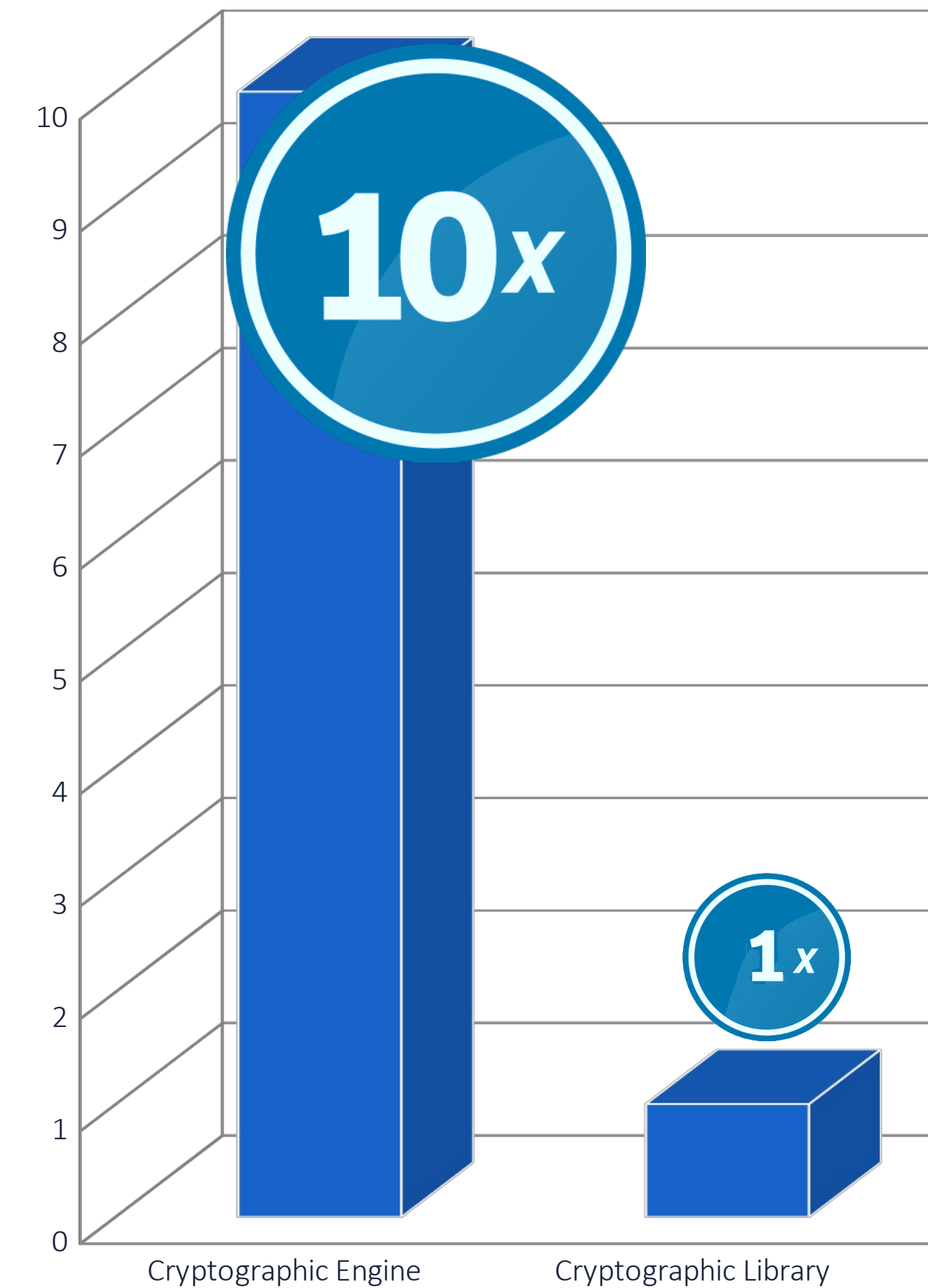


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Why security in HW is better than SW?

- ✓ **Faster:** HW cryptography performs the encryption and decryption many times the speed of SW implementation,
- ✓ **Higher Performance, Lesser Code size,**
- ✓ **Application Integrity Assurance:** hardware root of trust is more secure & higher assurance of code integrity over software,
- ✓ **Resistance to Reverse Engineering:** SF is more susceptible to RE
Resistance to Non-intrusive Attacks: SF is more vulnerable to attacks that are based on power consumption analysis
- ✓ **Higher level of Key Protection:** keys are stored in HW not in SF



Security vs hardware










✓ When implementing security countermeasures on an IoT device, best done using **hardware**

based security



Ready for the highest

Security level

	Software	Isolated Security IP (IPM)	HW-based security
Software attacks			
Micro-architectural attacks			
Physical attacks			

CryptOne

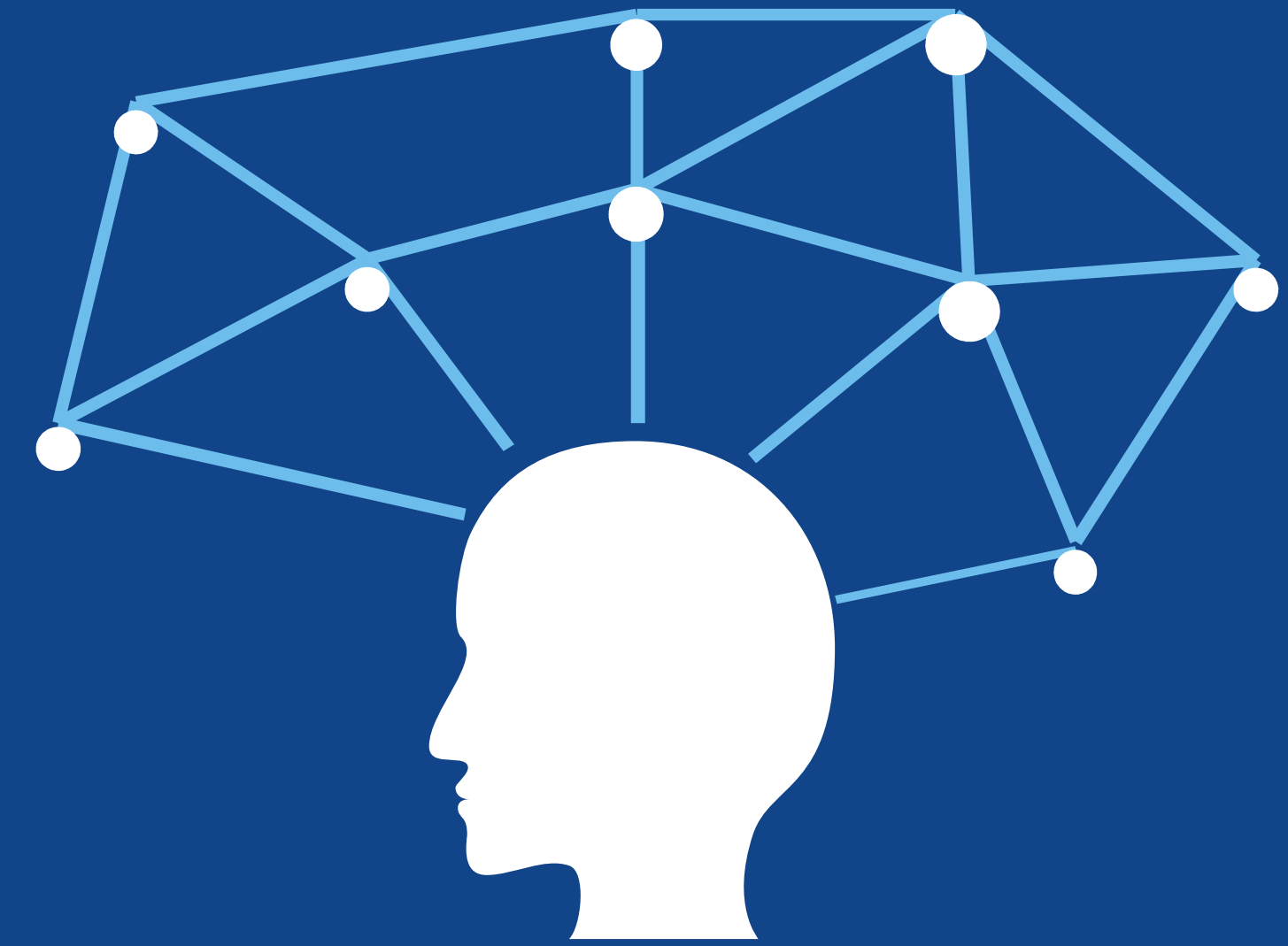
✓ CryptOne is a 100% safe crypto CPU, because...

It involves the use of **RSA asymmetric encryption scheme** to realize a cryptosystem with a **one-time pad (OTP)**,

✓ DCD's solution is a broadly defined crypto system solution based on an asymmetric RSA with a **hidden value of a component of a public key susceptible to crypto analysis and implementing the OTP rules**,

✓ Nowadays security is the key- that's why CryptOne OTP offers the **advantages of symmetric crypto systems with one-time pad while retaining the advantages of asymmetric systems.**

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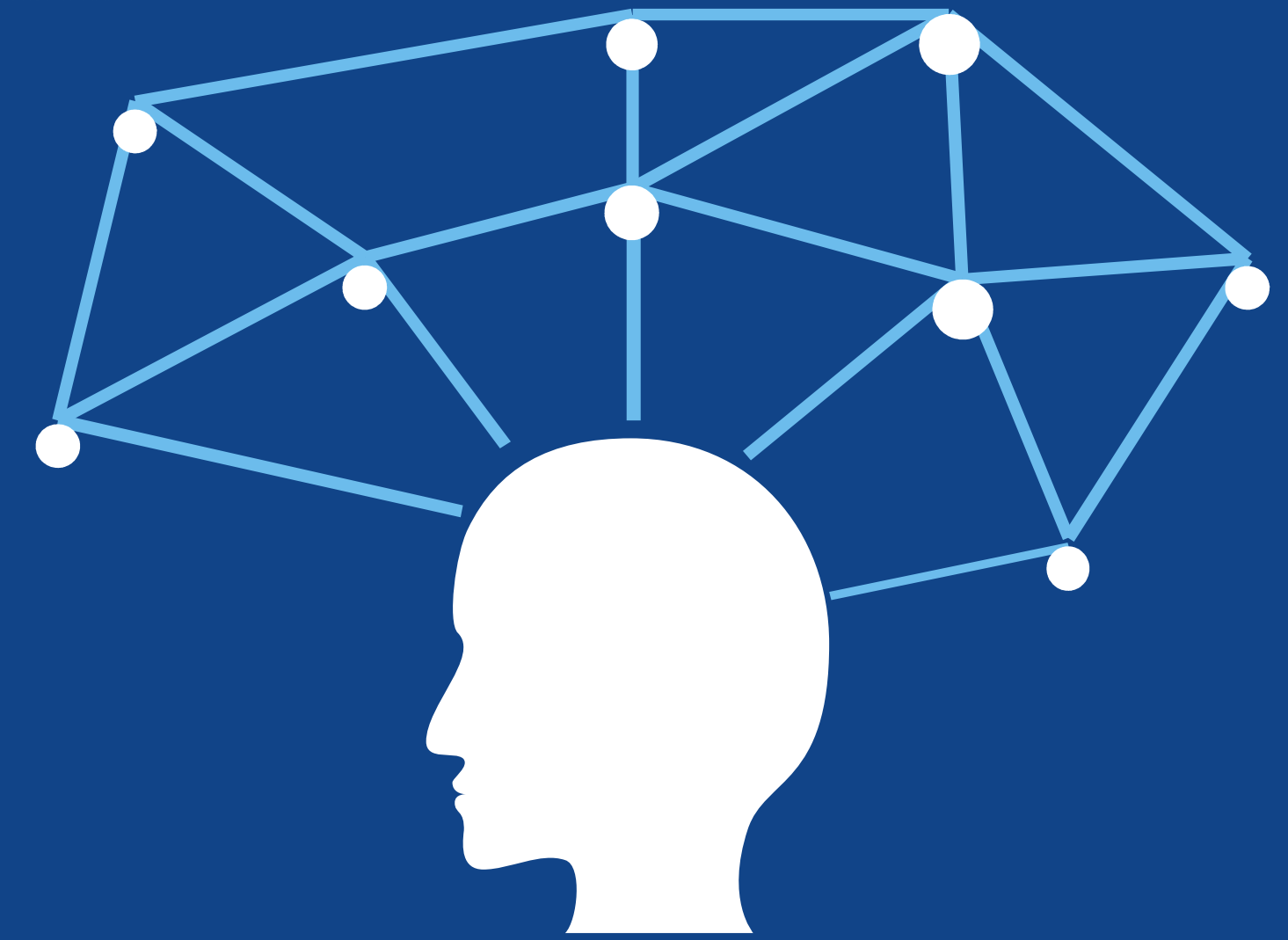


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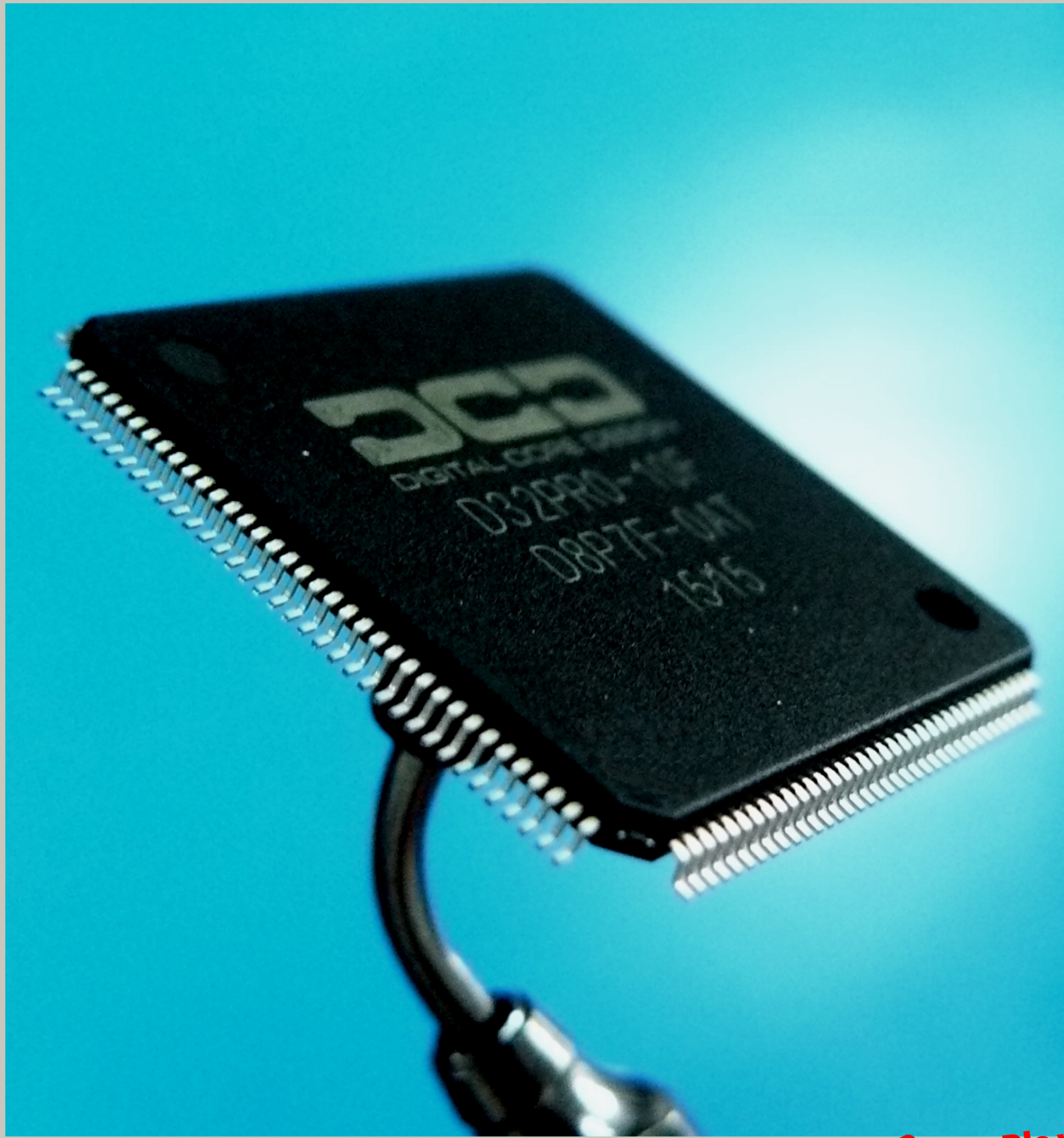
CryptOne

- ✓ CryptOne core is a universal, fully scalable solution which is able to boost asymmetric cryptographic algorithms like: **RSA, Diffie-Hellman** and **ECC**;
- ✓ It provides the efficient solution for asymmetric cryptography **boosting arithmetic operations** like: **modular exponentiation, multiplication, inversion, GCD finding** as also **point doubling**;
- ✓ The **energy efficient architecture** of CryptOne IP core enables the usage of the **very small silicon footprint** with **high processing speeds**.

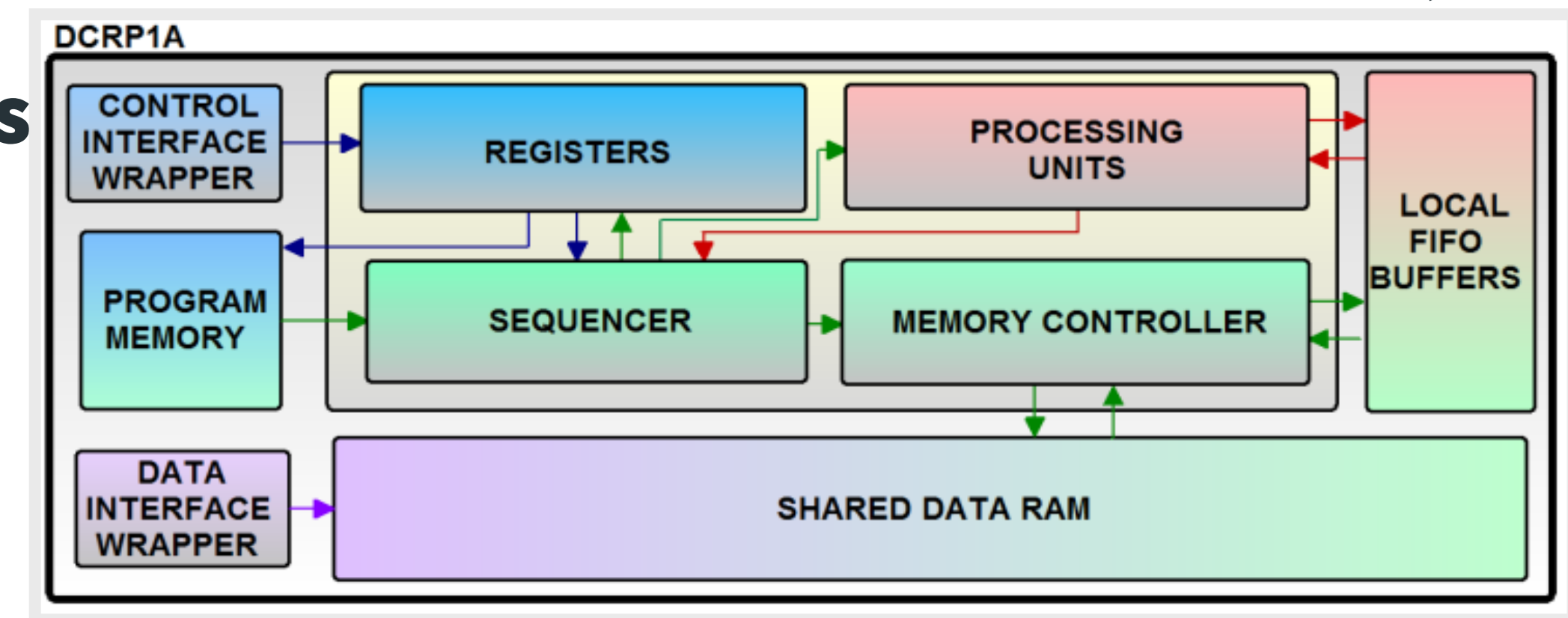
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100% safe crypto CPU



- ✓ CryptOne can be provided with **various different interfaces including AMBA AHB, AXI4, APB;**
- ✓ Very **intuitive interface** enables the fast, **straightforward system integration;**
- ✓ The core is **resistant to the Differential Power Attacks (DPA) and timing attacks**



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CryptOne's features:

- **CryptOne constant time algorithms:**
 - Modular exponentiation,
 - Parallel modular exponentiation CRT
 - ECDSA sign/verify
 - ECDH
 - Elliptic curve point multiplication
 - Modular multiplicative inverse
 - GCD
 - Modular reduction
 - Multiplication
 - Division
- **Cryptographic algorithm applications:**
 - ECDSA, ECDH
 - RSA key generation
 - RSA Sign/Verify/Encrypt/Decrypt
 - Diffie-Hellman schemes
 - Miller-Rabin Primality check
 - System applications:
- **Client-server communication:**
 - Sensor networks
 - SSL/TLS stacks
 - IoT devices
 - Embedded security/ID devices
- **AMBA AHB, AXI4, APB** interface ready
- Rapid & easy development with delivered API
- **Patent pending architecture**
- Algorithms resistant against SPA and timing attacks
- CryptOne elliptic curves with native support:
 - NIST P-192
 - NIST P-224
 - NIST P-256
 - NIST P-384
 - Koblitz P-192
 - Koblitz P-256
 - Koblitz P-384
 - Brainpool P-256
 - Brainpool P-384
 - Brainpool P-512
 - Other/custom curves optional support
- Software support:
 - **OpenSSL** engine
 - **MbedTLS** port
 - OS independent crypto library



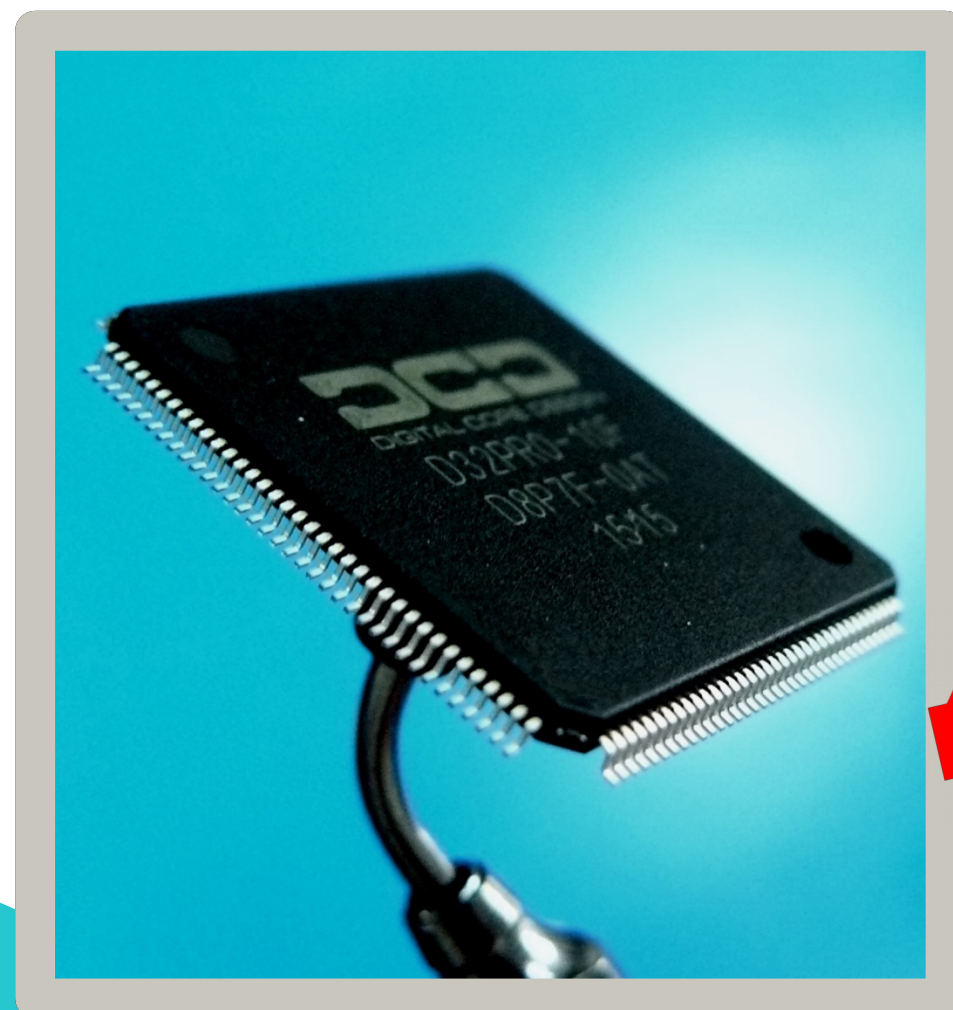
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CryptOne – choose the best for you

CryptOne EC

- Modular Exponentiation constant time operation algorithm support
- Parallel Modular Exponentiation CRT constant time operation algorithm support
- Secure private RSA key computation, no branch inversion
- Easy to use software library interface

- Elliptic Curves point multiplication constant time algorithm
- Constant time modular multiplicative inverse algorithm for private operations.
- Boost modular multiplicative inverse algorithm for public operations
- Native support for most popular elliptic curves
- Easy to use software library interface



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CryptOne TLS

- Modular Exponentiation constant time operation algorithm support
- Modular Exponentiation CRT constant time operation algorithm support
- Secure private RSA key computation, no branch inversion
- Elliptic Curves point multiplication constant time algorithm
- Constant time modular multiplicative inverse algorithm for private operations
- Boost modular multiplicative inverse algorithm for public operations
- Native support for most popular elliptic curves
- Modular Reduction constant time algorithm
- Greatest Common Divisor algorithm
- MbedTLS and OpenSSL port libraries
- Software interface and examples for building own hardware algorithms with support for:
 - Large vector addition/subtraction
 - Large vector shift right/left
 - Large vector modular multiplication
 - Branch, execution flow controls

100% safe crypto CPU

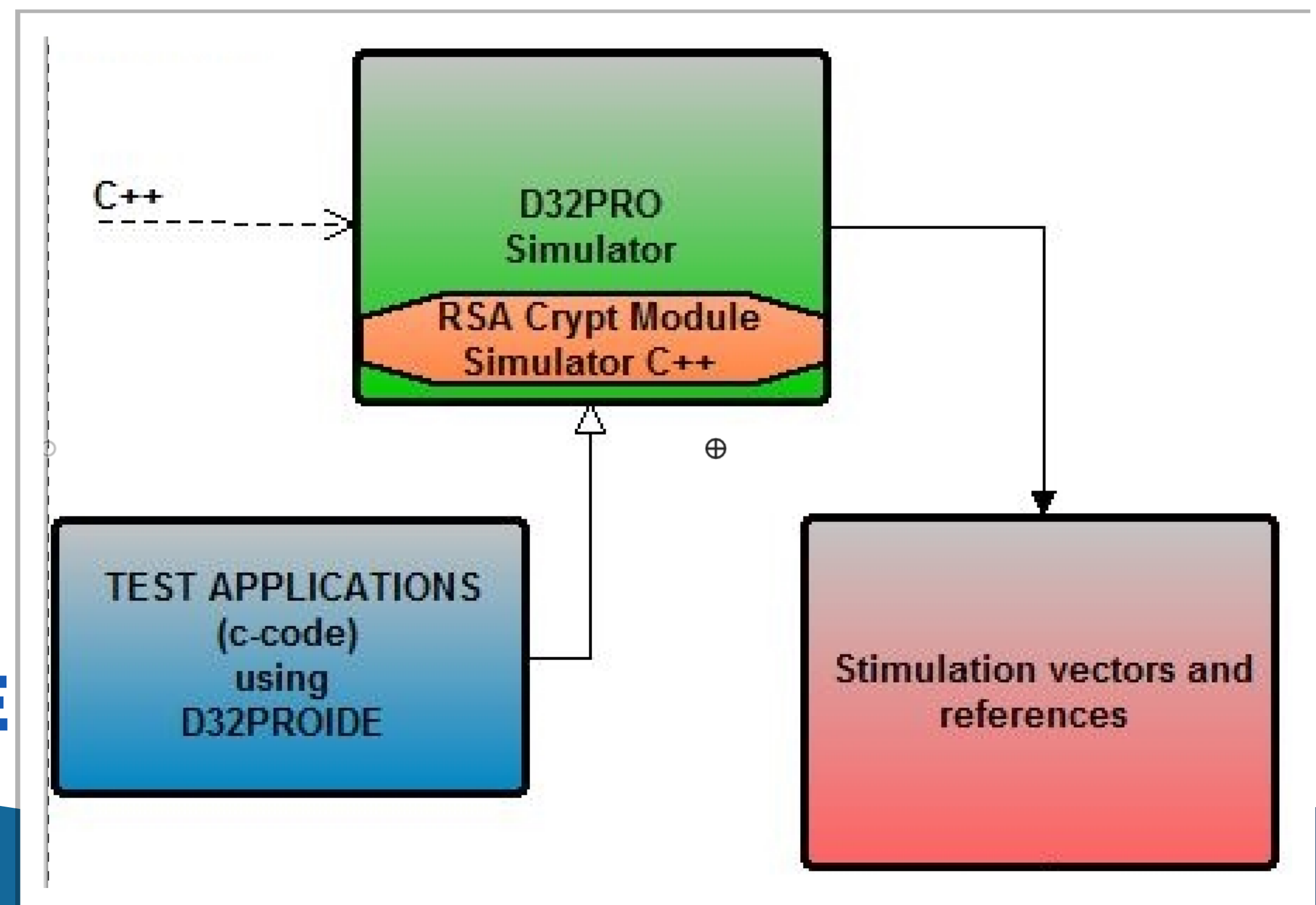
✓ CryptOne consists of technologically independent hardware crypto processor in the form of synthesizable IP Core module prepared for integration and implementation in an IC (ASIC or FPGA)

CryptOne offers both software

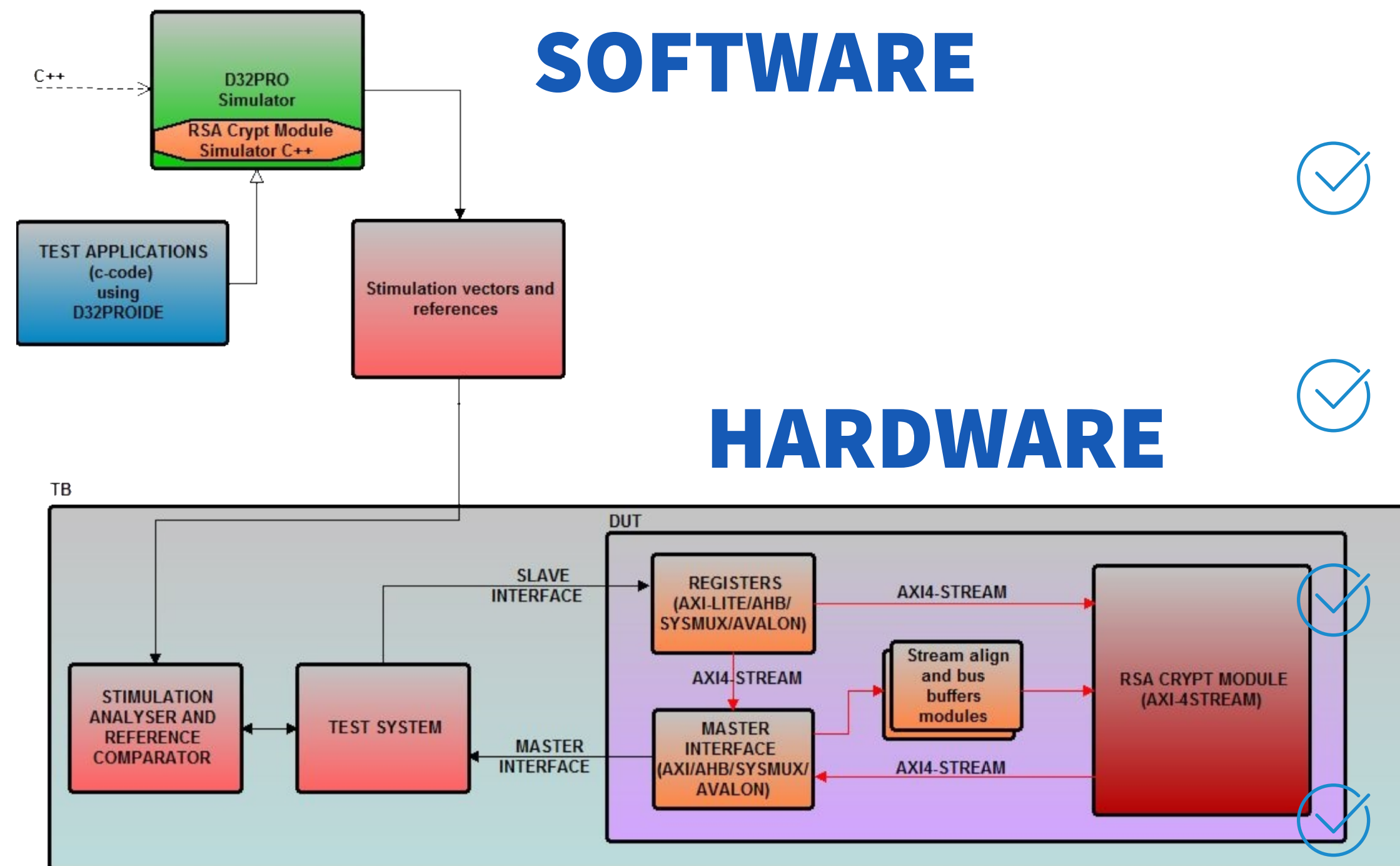
✓ and hardware cryptography advantages

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SOFTWARE



CryptOne

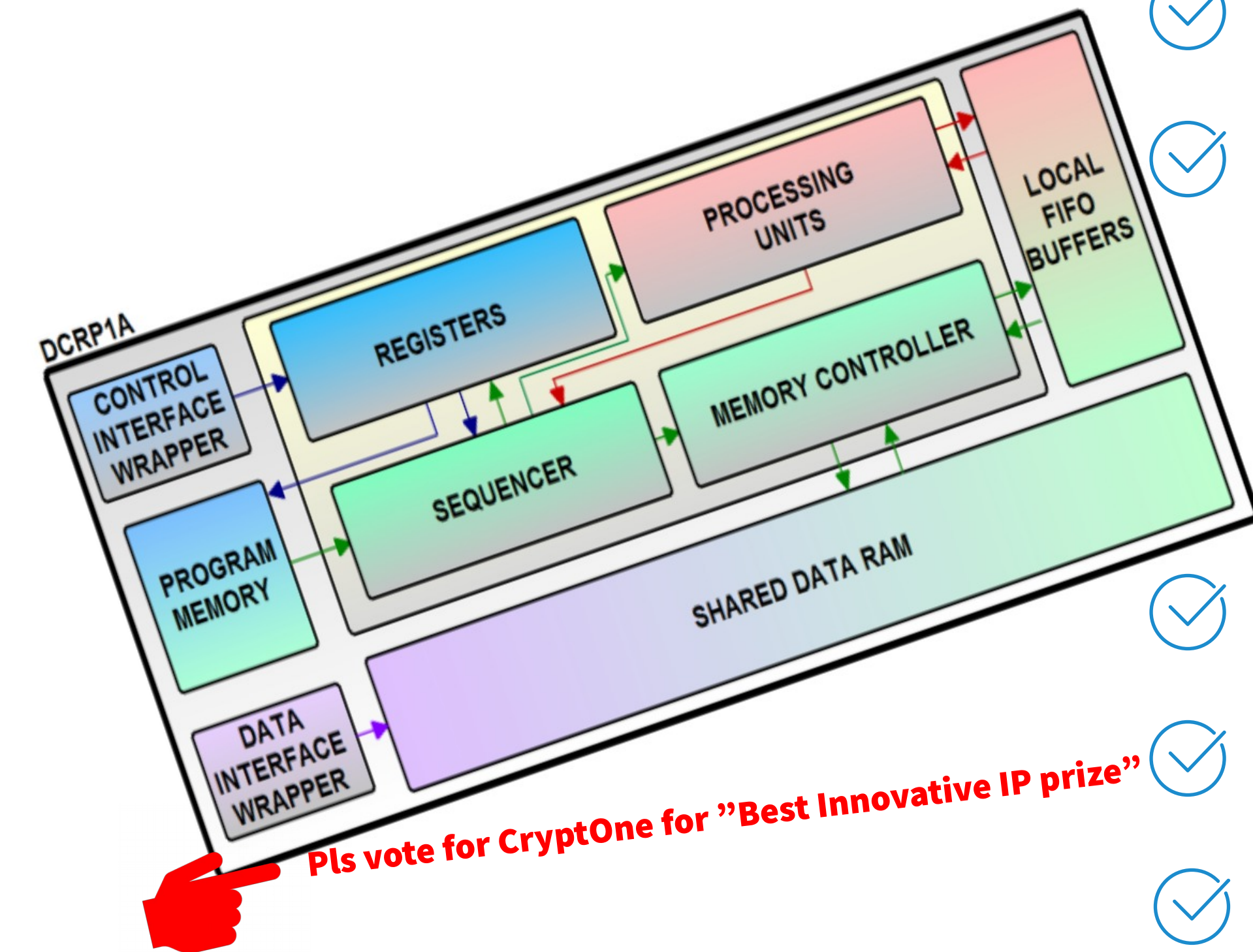


- ✓ Hardware & software co-design
 - ✓ Test stimulation vectors are generated with the usage of D32PRO Simulator – the sub-module for RSA crypter is written in C++
 - ✓ All tests are written in C using D32PRO software – they can be easily used in hardware through D32PRO platform
 - ✓ Generated stimulation vectors are also used for reference comparison
- All internal data is exchanged through the AXI4-Stream protocol in simple format = higher flexibility
- The internal RSA CRYPT MODULE can work in a separate domain

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Receiveables

- ✓ C software drivers with API
- ✓ Silicon proven architecture
- ✓ Hardware code:
 - VERILOG Source Code or
 - FPGA Netlist
- ✓ VERILOG test bench environment
- ✓ Technical documentation
- ✓ Synthesis scripts
- ✓ 12 months of free technical support included



Summary

 Pls vote for CryptOne for "Best Innovative IP prize"

✓ Success stories are the best confirmation for DCD's quality




Reference letter for Digital Core Design



"We once licensed DP8051 from DCD. When we needed incorporating a CAN 2.0B controller in our another chip, we firstly thought about DCD. After a short period evaluation, we decided to adopt DCD's DCAN IP and we found the core to be well designed, well documented. We are satisfied in DCD's support and price."

-- Liuyadong, SOC Director of




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And System programming
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DP-16-0017
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Reference for Digital Core Design



Dear Sir,

"As one of the first companies we had the chance to work the newest DCD's invention - the D32PRO in a project involving implementation of an extensive architecture (D32PRO + DFPAU + DUSB2-ULPI + DMAC-RMII + DQSPI + DI2CM + DCAN + DLIN + DMART+ DBLCD32).

Despite the complexity of the design, all DCD's modules turned out to be easy to work on, so we have completed the works without any difficulties or delays. At all times DCD's team assisted in the process providing reliable and always on time support. Working with DCD proved to be a very rewarding experience for all involved; this should result in a solid foundation for future collaborations."



DCD Recommendation

Flowserve purchased and used some DCD IP (DFPIC1655X with DSPI and DI2CM) for a chip that we are using in a product scheduled to launch at the beginning of next year (2018). This project has been underway since 2015. We have had ample time to thoroughly test DCD's IP and have found it to be solid. We have no known issues.

Flowserve also asked DCD to make some minor tweaks to the IP for us and to provide some implementation specific updates to the documentation they provided and have found DCD to be responsive, helpful, and easy to work with.

Flowserve will have no problem using DCD in the future and would recommend them to other companies considering using their IP.

Thanks,
Nathan Higbee
R&D Engineer

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Hsinchu, Taiwan 300 R.O.C.
Tel : 886.3.578.7888 Fax: 886.3.578.7889



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2nd November 2017

To whom it may concern,

Our cooperation with Digital Core Design lasts from 2013, since then, we have built several projects using their IP Cores. Our products incorporate DCD's DQ8051CPU, DUSB2, DQSPI, DCAN, DMAC, D16950, DI2CS and DI2CM. Based on those four years of cooperation I can say without a doubt, that DCD is a very capable and professional team, their workmanship, quality, knowledge is among the best. I highly recommend Digital Core Design.

Yours Sincerely,





SIEMENS

Siemens AG, DP MC-RTS R&D H&L2, Siemensstrasse 1, 91034 Erlangen	Name: Frank Dietrich	DP MC-RTS R&D H&L2
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	Fax: +49 (0) 171 475-2154	frank.dietrich@siemens.com
	E-mail:	
	Our reference ID:	FD
	Date:	November 1, 2017

D16950 UART core appreciation

Target:
Core: D16950 UART core with 16Byte FIFO from DCD
Order date: April 2014
License type: Netlist, single site option, proper for Altera CycloneIV devices

Shipment:

- License file via Email
- D16950-setup-see (includes core specification & VHDL source files) via download link

Usage:
The D16950 core from DCD with 16 Byte FIFO is used in industrial FPGA design with about 10.000 units per year. This core supplies an RS232 customer interface.
The D16950 core comes with a parallel port interface that can easily adapt to the standard Altera Avalon bus system. Baud clock and core clock can be supplied by different clock sources. With a baud clock of 1.8432 MHz the typical baud rates on RS232 interfaces can be provided.

Siemens AG
Digital Factory Division Management, Jan Meinhart
Digital Factory Division Management, Tübingen/Hamburg

Siemensstrasse 1
91034 Erlangen
Germany

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E-mail: frank.dietrich@siemens.com

Why DCD?

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✓ Two decades of IP Core market experience

The Peninsula

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Qatar GCC Middle East International Stock Market

First Polish 32-bit microprocessor

September 11, 2015 - 12:00:00 am



Polish Vice-Minister of Economy Arkadiusz Bak (left) and chairman of Digital Core Design company Jacek Hanke unveil the first Polish 32-bit microprocessor (D32PRO) at the Ministry of Economy in Warsaw yesterday. The CPU is an innovation of

First Polish 32-bit microprocessor

September 11, 2015 - 12:00:00 am



Design & Reuse

SEARCH IP NEWS INDUSTRY ARTICLES BLOGS VIDEOS
D32PRO, fully scalable & royalty-free 32-bit CPU from DCD

, September the 17th, 2015 -- Digital Core Design presents a System-on-Chip design house from Poland

D32PRO, pierwszy polski procesor 32-bitowy
Spółka Digital Core Design z Bytomia zaprezentowała pierwszy polski procesor 32-bitowy - D32PRO.

Pierwszy polski procesor 32-bitowy

Anna Bielak, 2015-09-10 11:00

CNC 新华网电视台
百度寻人 寻找每个流浪在外的孩子 齐心协力 让孩子早日回家
波兰推出第一款32位微型处理器(1/2) 2015-09-11 18:23



Why DCD?

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- ✓ DCD presented World's fastest 8051 CPU during CeBIT 2013 official opening ceremony (in front of German Chancellor A. Merkel and EU President D. Tusk)
- ✓ D32PRO has been presented during EXPO in Milan and Hannover Messe

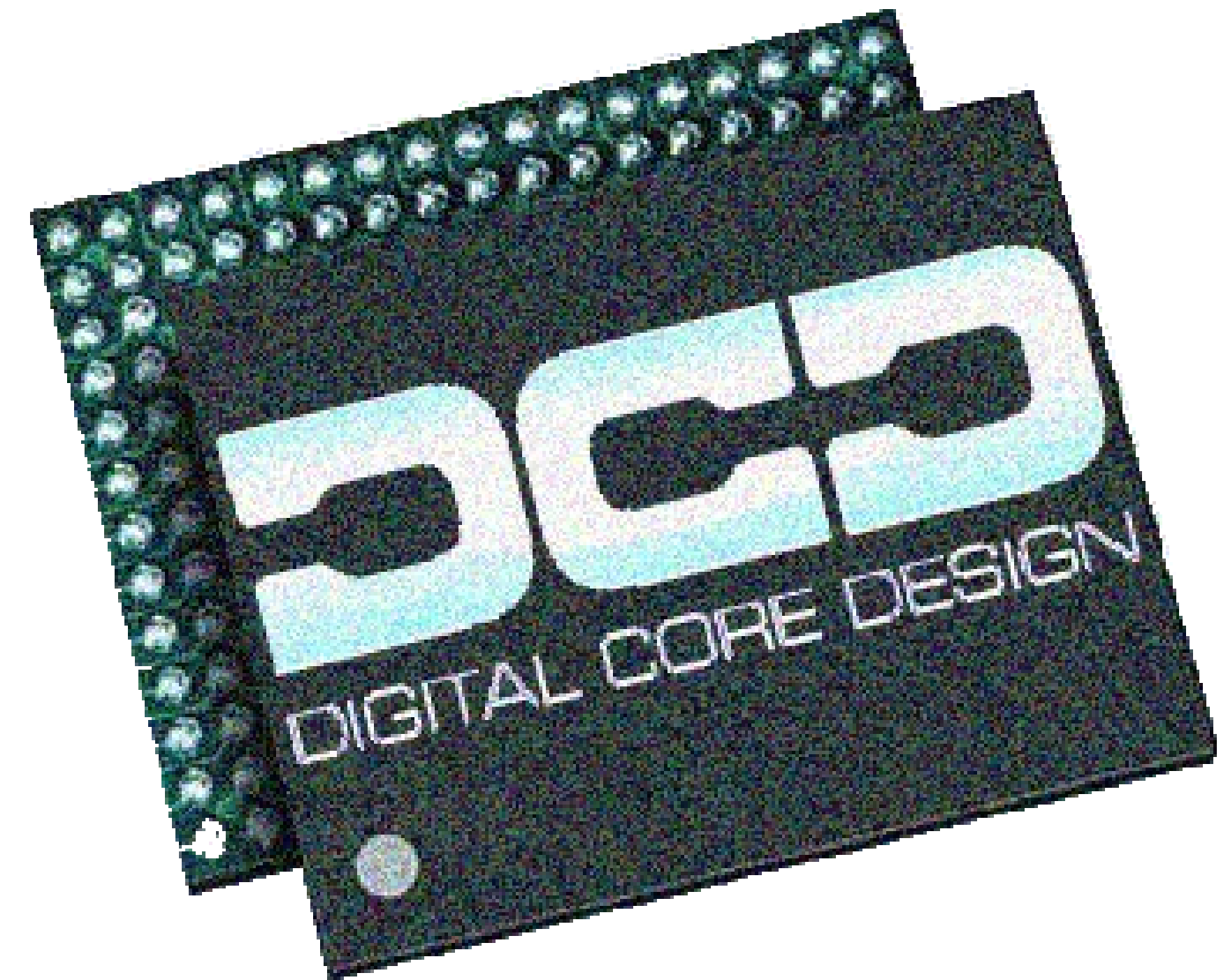


Why DCD?

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- ✓ Innovative products – always step ahead before competitors
- ✓ Know-how based on two decades of market experience
- ✓ Optimal solutions which answers market needs
- ✓ Significant Time-to-market reduction
- ✓ Coherent IP Core portfolio
- ✓ IP Cores tailored to the project needs
- ✓ Complete solution from one company

like e.g.: IP Core + debugger + ...



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Thank you!

Any questions?

info@dcd.pl

